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FORM PTO-1449	SERIAL NO. 09/748,589	CASE NO. 10519-9
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE December 22, 2000	GROUP ART UNIT 2187
(use several sheets if necessary)		APPLICANT(S): March et al.

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER <small>Number-Kind Code (if known)</small>	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
GJP	A1	6,480,463 B2	11/2002	Hunter et al.		
↑	A2	6,336,175 B1	01/2002	Shaath et al.		
↑	A3	6,446,177 B1	09/2002	Tanaka et al.		
↓	A4	5,065,389	11/1991	Roth		
↓	A5	6,321,360 B1	11/2001	Takeuchi et al.		
GJP	A6	6,377,526 B1	04/2002	Vining et al.		

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GJP	A1	US 2002/0034105 A1	03/2002	Kulkarni et al.		
↑	A2	5,559,778	09/1996	Inokuchi et al.		
	A3	5,119,291	06/1992	Flannagan et al.		
↓	A4	5,321,824	06/1994	Burke et al.		
	A5	5,448,728	09/1995	Takano et al.		
GJP	A6	5,761,741	06/1998	Robbins et al.		

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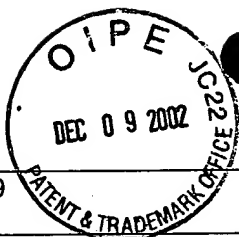
FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER <small>Number-Kind Code (if known)</small>	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES OR NO
GJP	A7	EP 1 168 150 A1	02/2002	EPO		

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)	
GJP	A8	NN8803151, IBM Technical Disclosure Bulletin, "Incremental Directory Indexes for Write-Once Media", March, 1988, Vol. 30, Issue 10, pp. 151-155.

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES	NO

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)	
GJP	A1	"Jack St. Clair Kilby," Jones Telecommunications & Multimedia Encyclopedia, http://www.digitalcentury.com/encyclo/update/kilby.html , 4 pages (1999).
	A2	"The 'Chip' Inventors (part 1)," Dan Murray, http://www.livingstonmontana.com/access/dan/150thechipinventors-1.html , 3 pages (1999).
	A3	T. R. Reid, "The Chip: How Two Americans Invented the Microchip and Launched a Revolution," pages 22-23 (1984).
	A4	"About Jack" http://www.ti.com/corp/docs/kilbyctr/jackbuilt.shtml , 6 pages (2002).
	A5	"Definition: integrated circuit," http://www.its.bldrdoc.gov/fs-1037/dir-019/2755.htm , 1 page (1996).
	A6	Meindl, "Definitions of Terms for Integrated Electronics," IEEE Journal of Solid-State Circuits, page 2 (March 1967).
	A7	Muller et al., "Device Electronics for Integrated Circuits," second edition, pages 65 and 106 (1986).
	A8	"3D-ROM - A First Practical Step Towards 3D-IC," Zhang, 7 pages, July 2000.
GJP	A9	"Construction Techniques for Systematic SEC-DED Codes with Single Byte Error Detection and Partial Correction Capability for Computer Memory Systems," Penzo et al., IEEE Transactions on Information Theory, Vol. 41, No. 2, March 1995, pages 584-591.

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GJP	A1	5,437,028	07/1995	Iijima		
GJP	A2	5,890,169	03/1999	Wong et al.		
GJP	A3	6,000,023	12/1999	Jeon		

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